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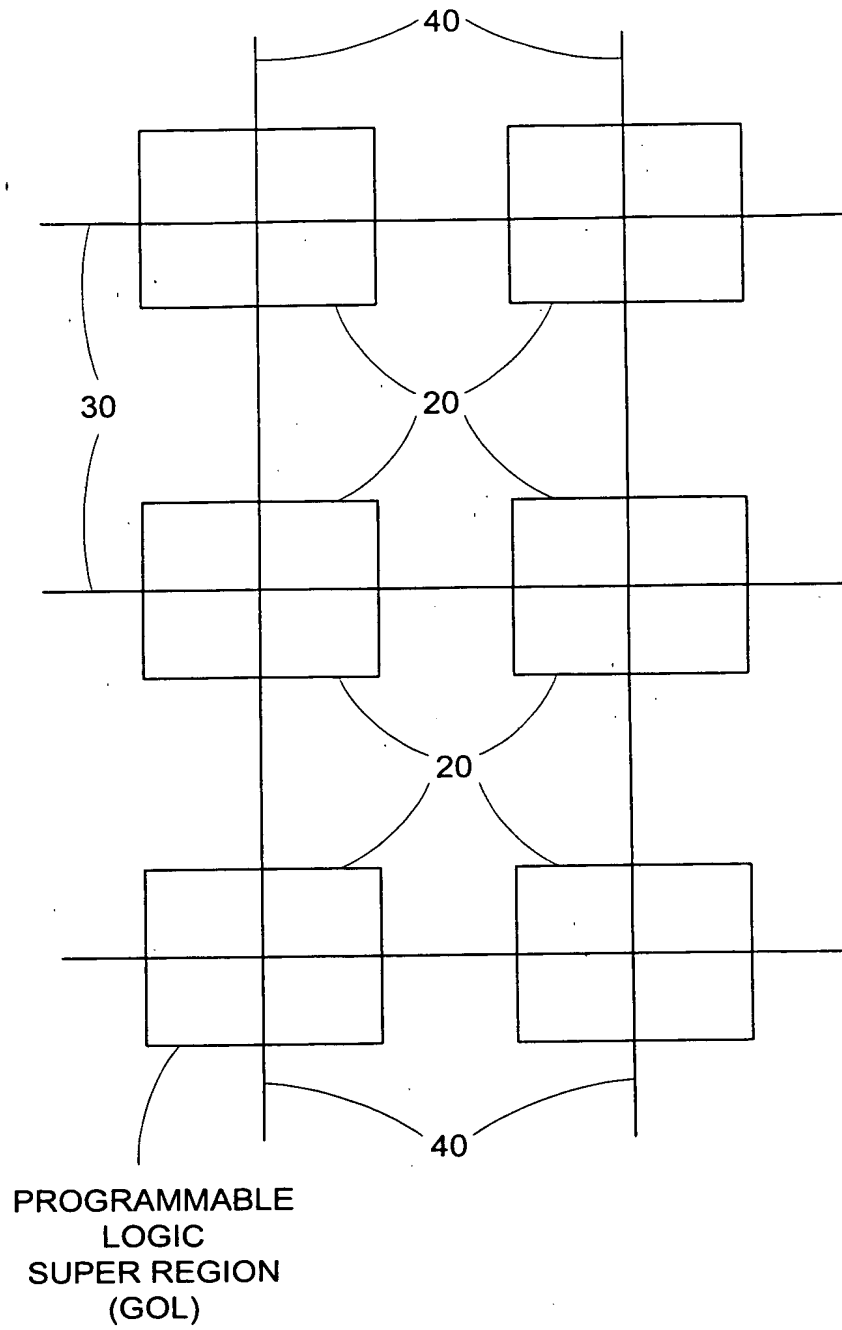


FIG. 1

GROUP OF LOGIC ARRAY BLOCKS (GOL)
(PROGRAMMABLE LOGIC SUPER-REGION)

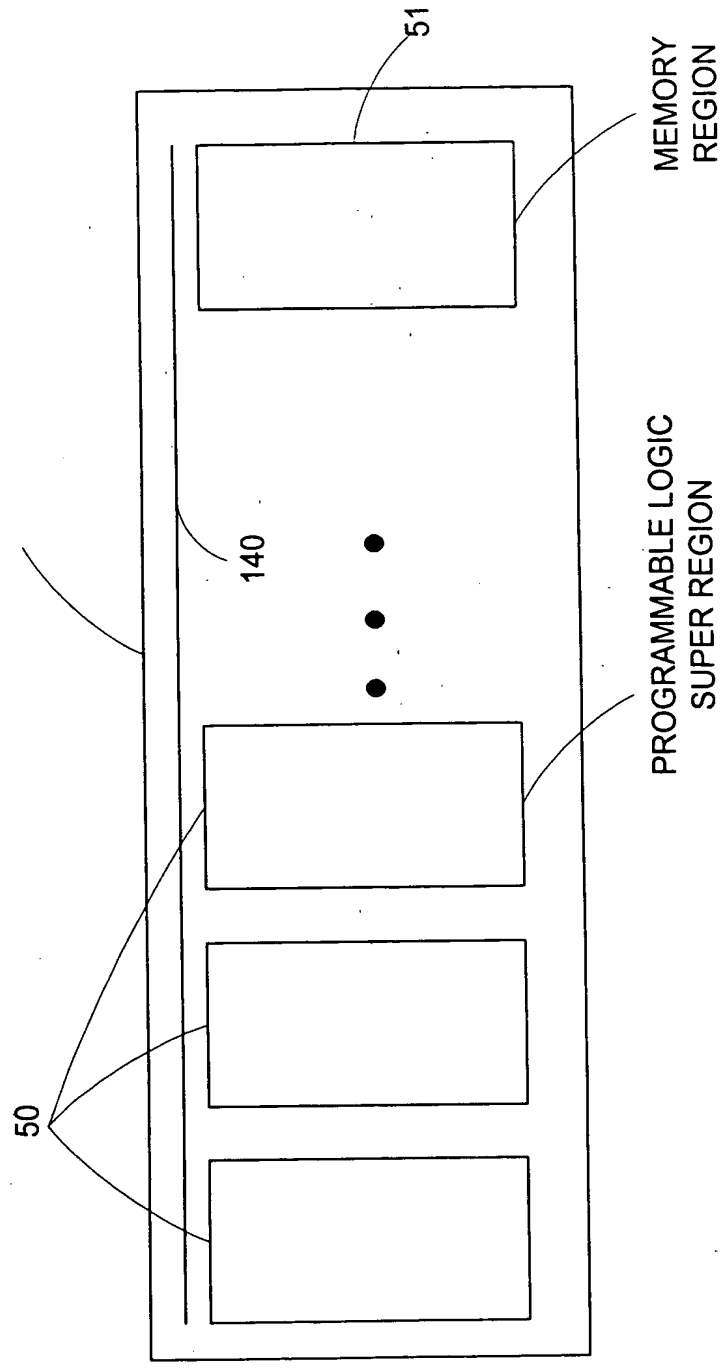
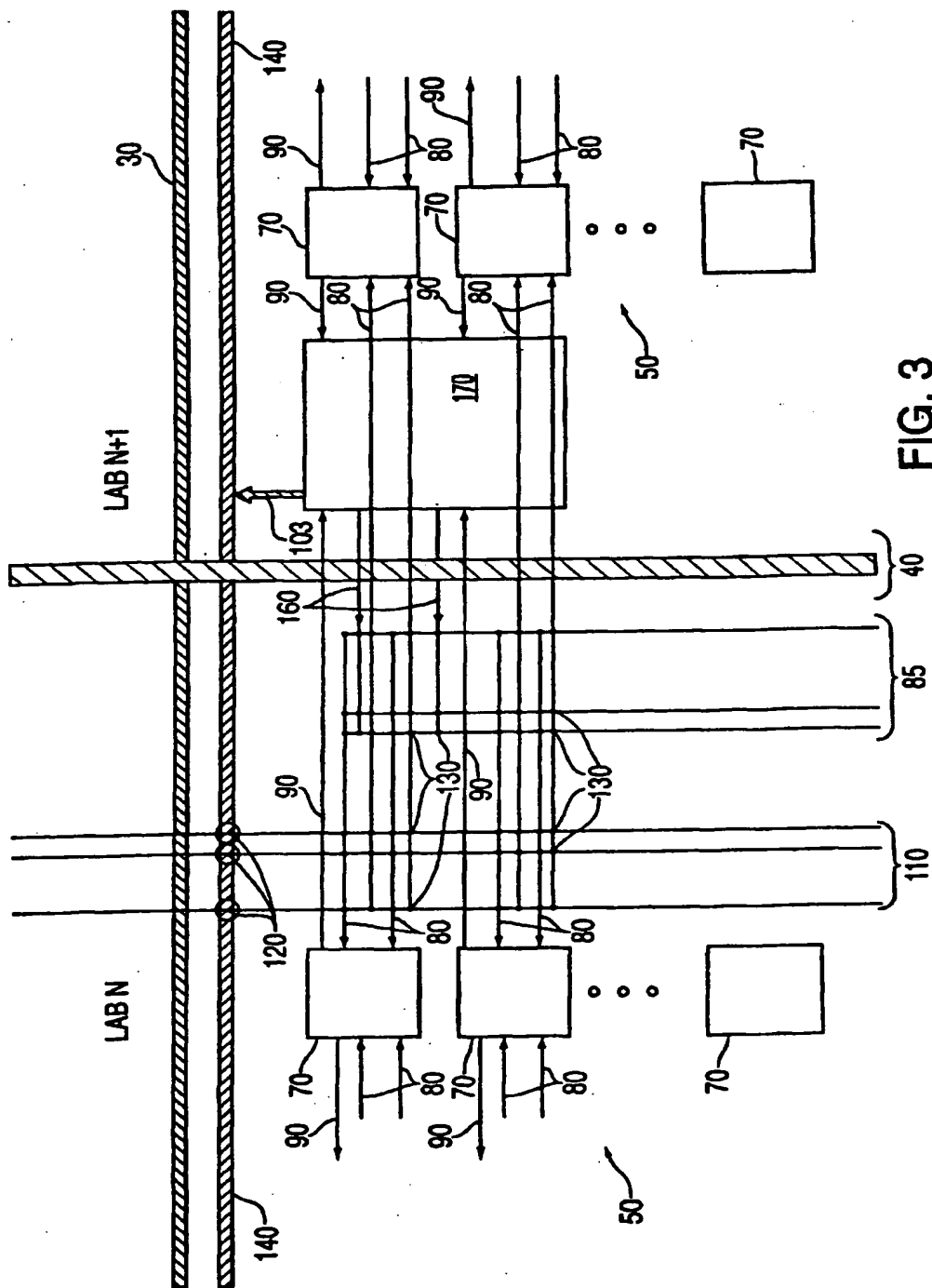


FIG. 2



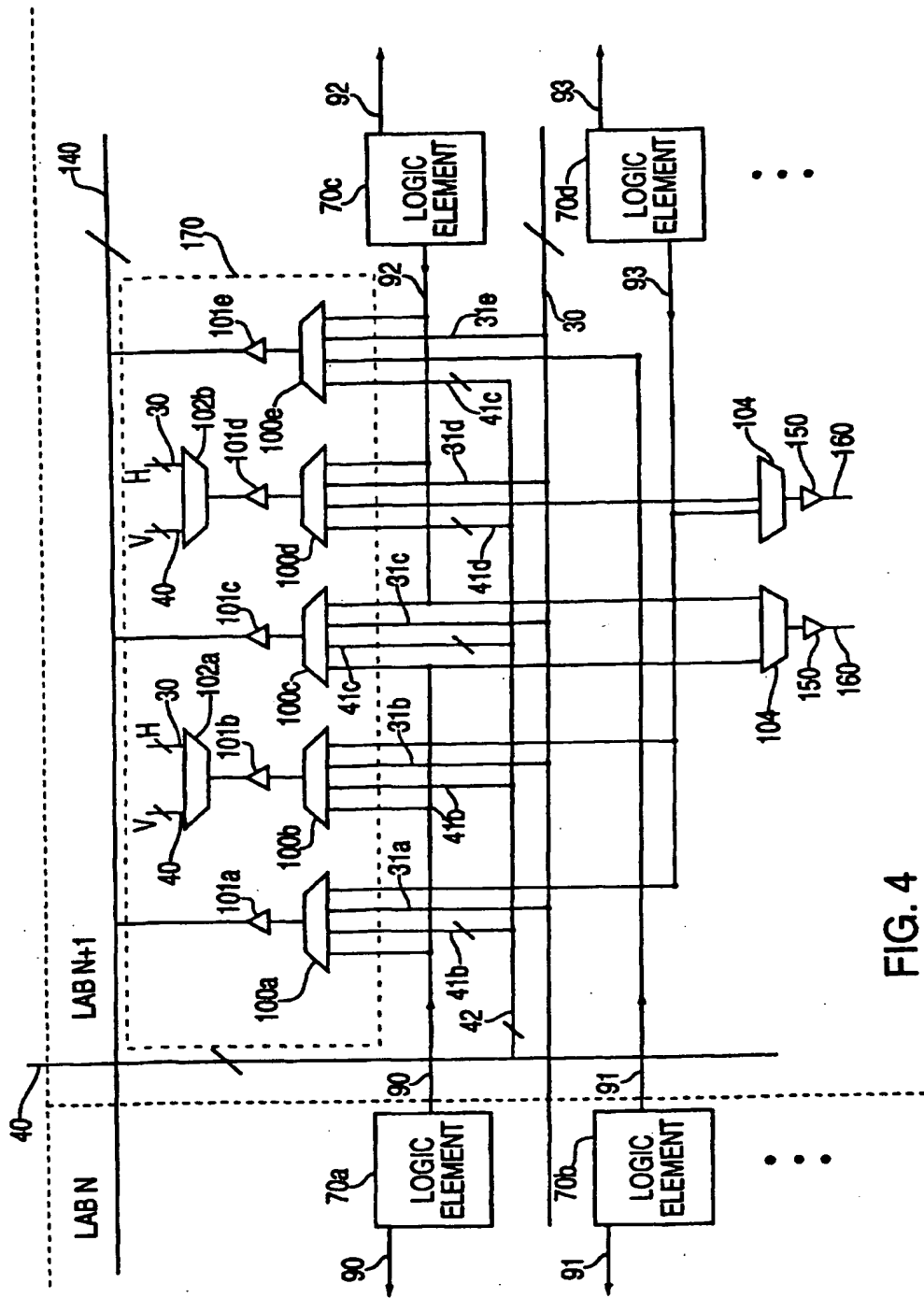


FIG. 4



FIG. 5

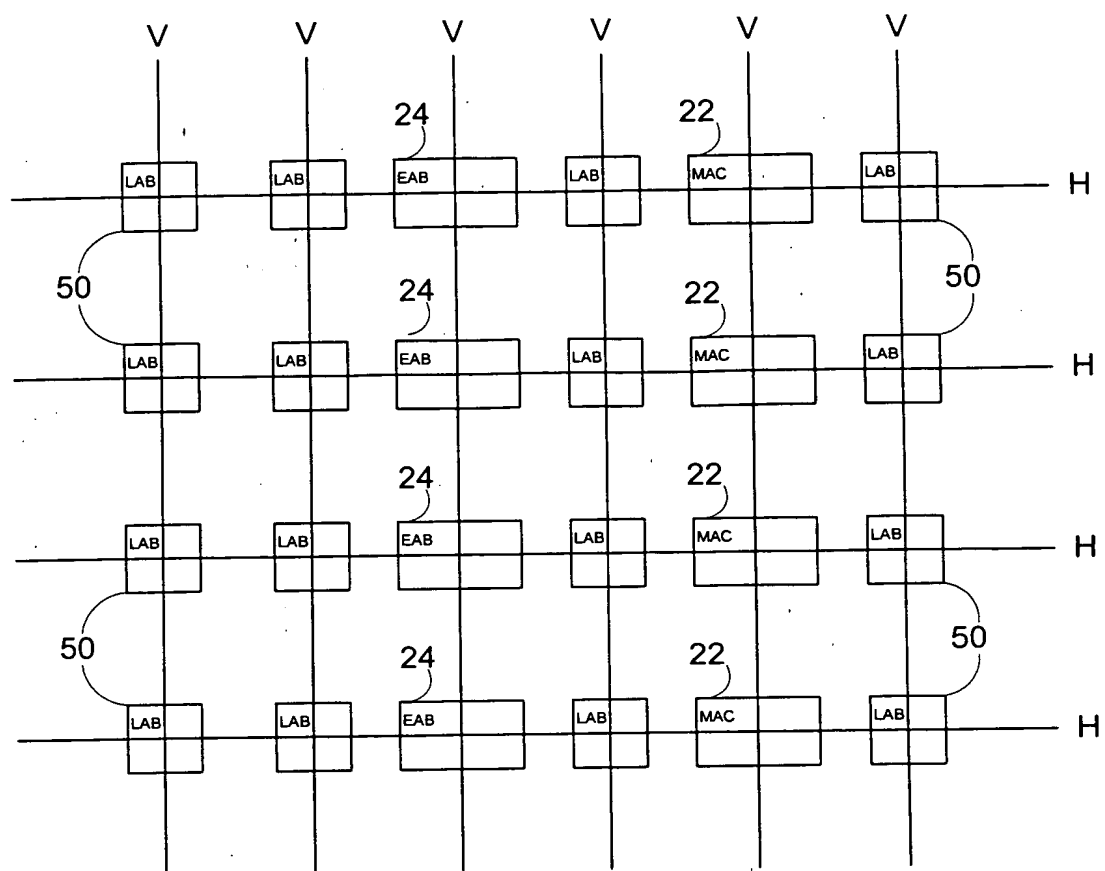


FIG. 6



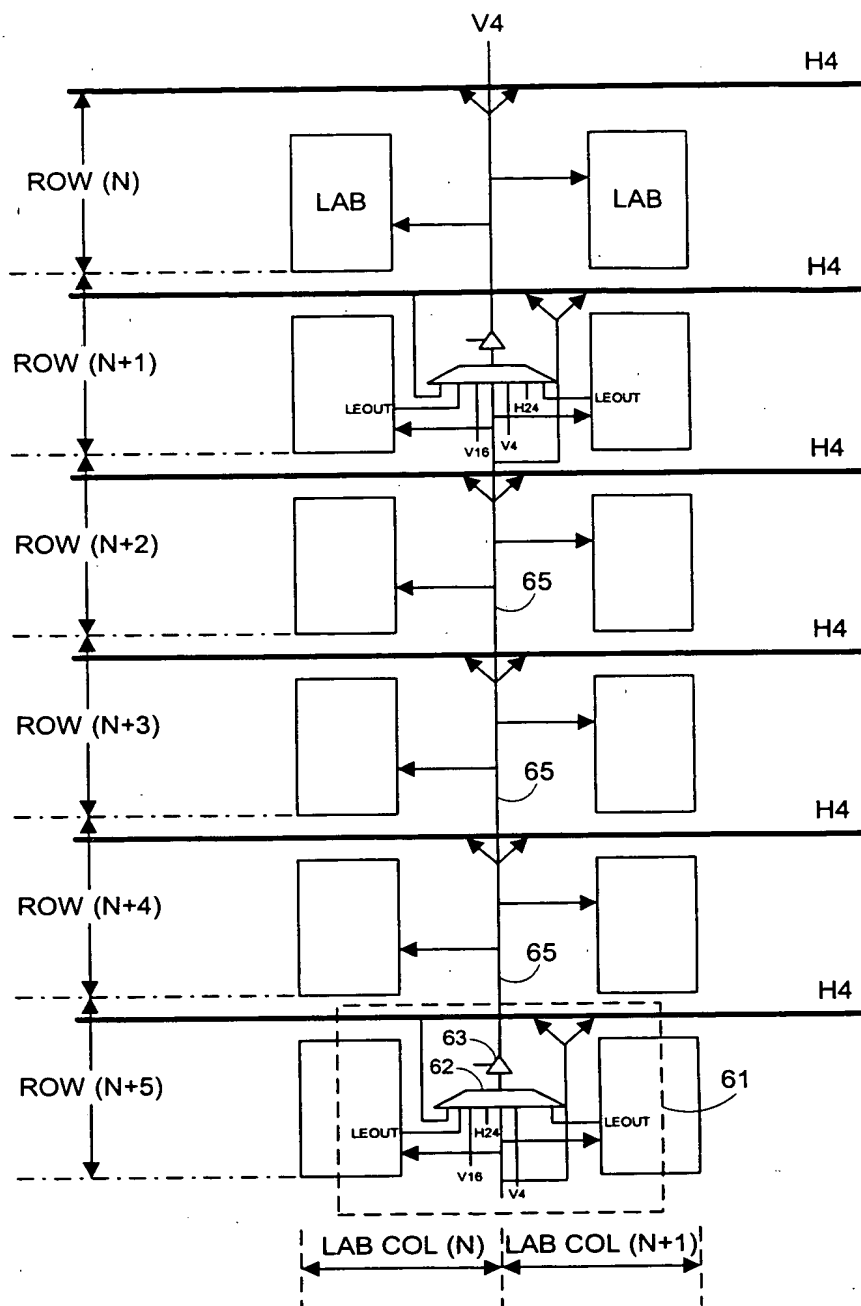


FIG. 8

**TWO INPUT
LOOK-UP TABLE**

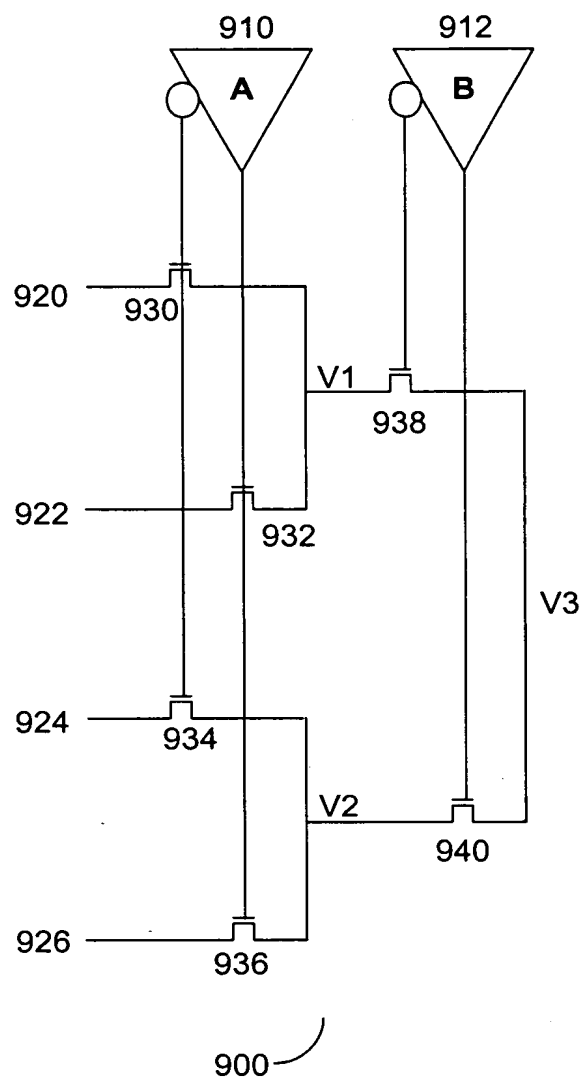


FIG. 9

FOUR INPUT LOOK-UP TABLE

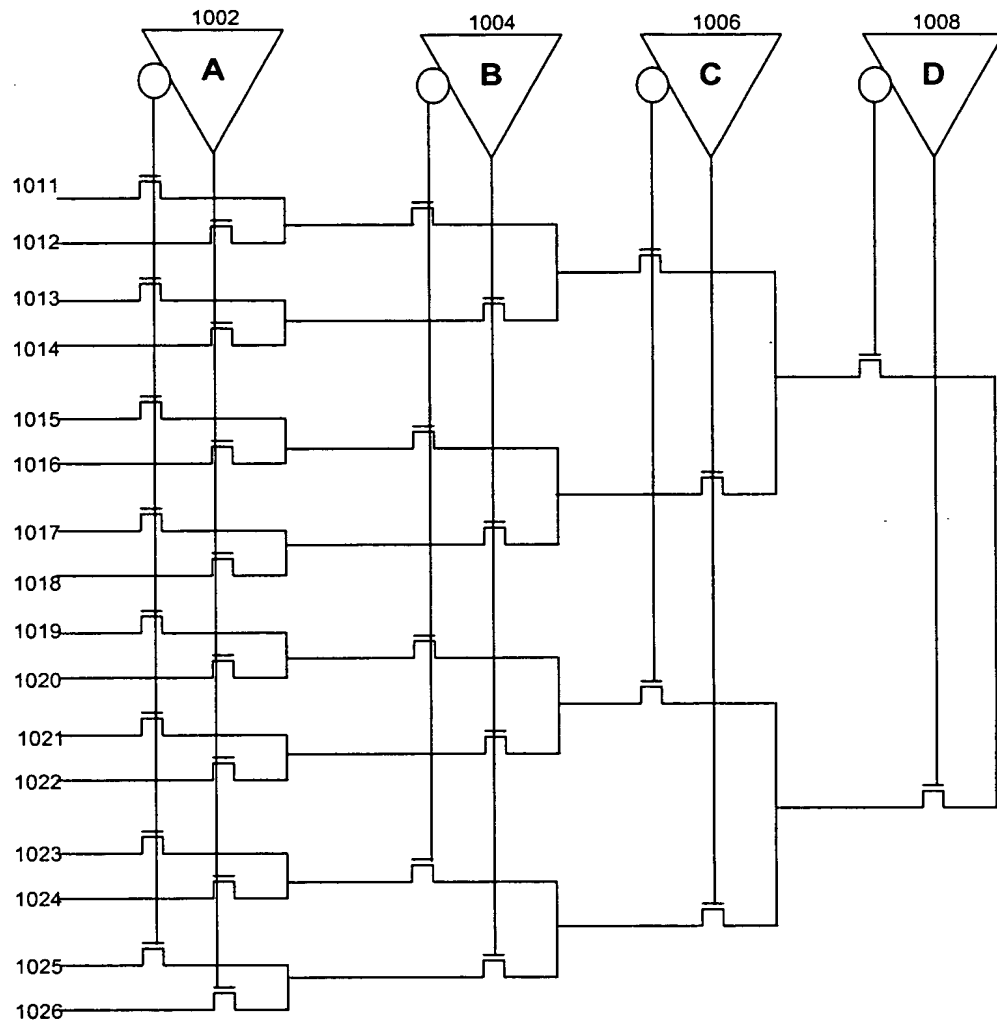


FIG. 10

1105

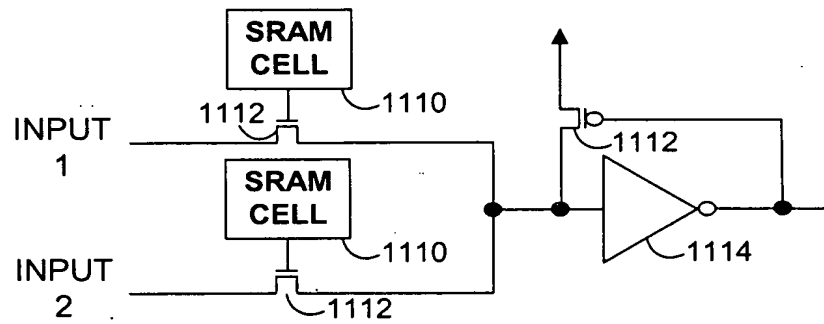


FIG. 11

1205

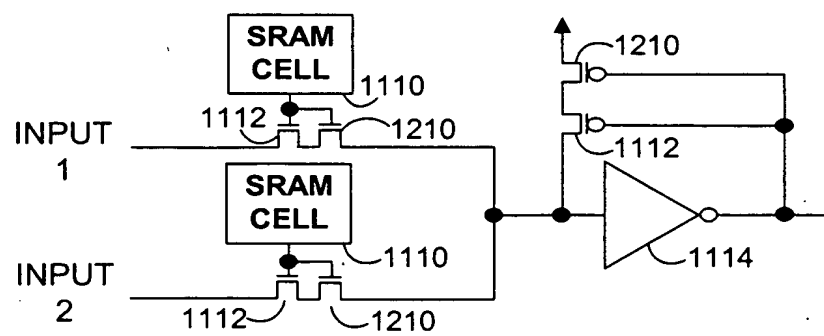


FIG. 12

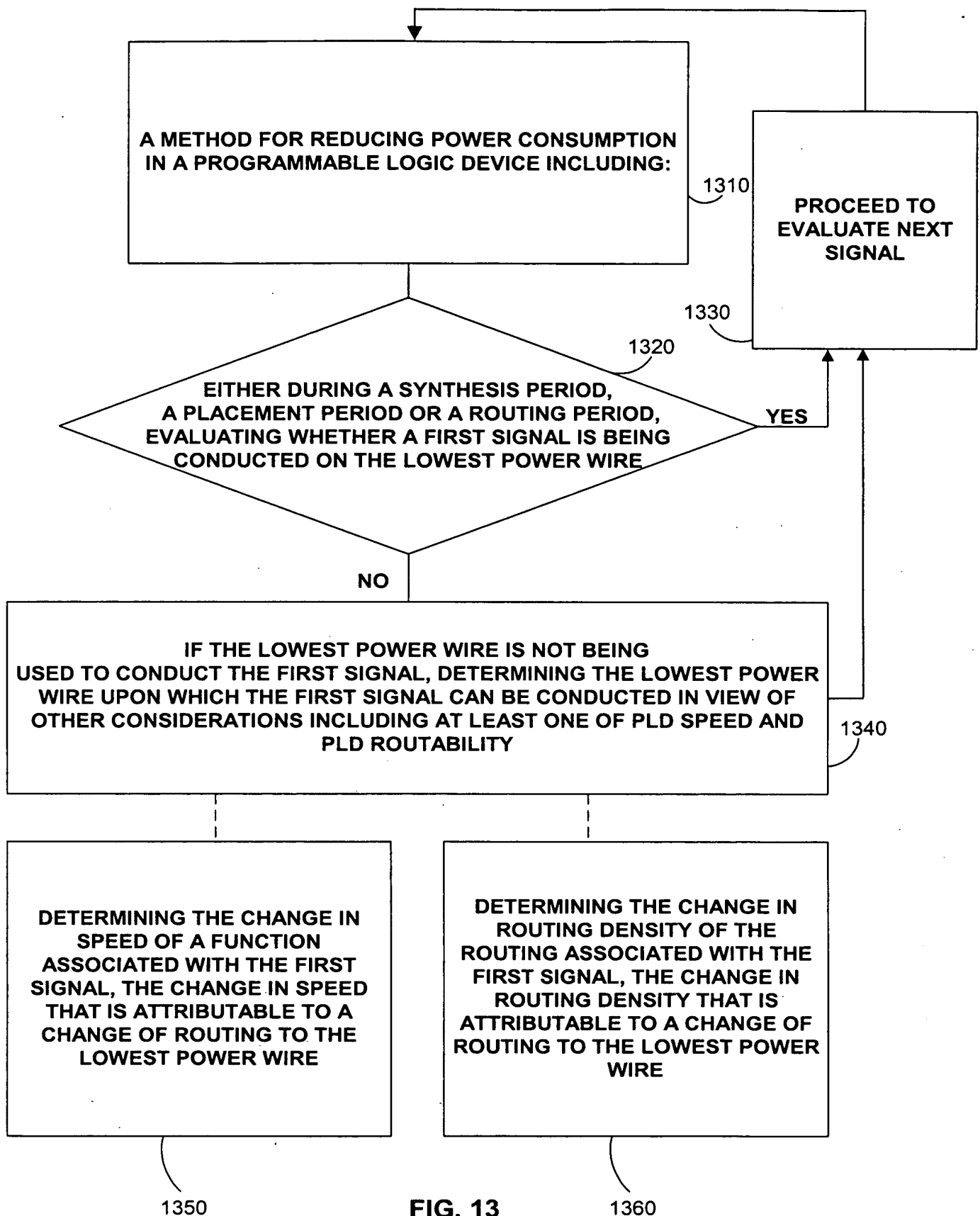


FIG. 13

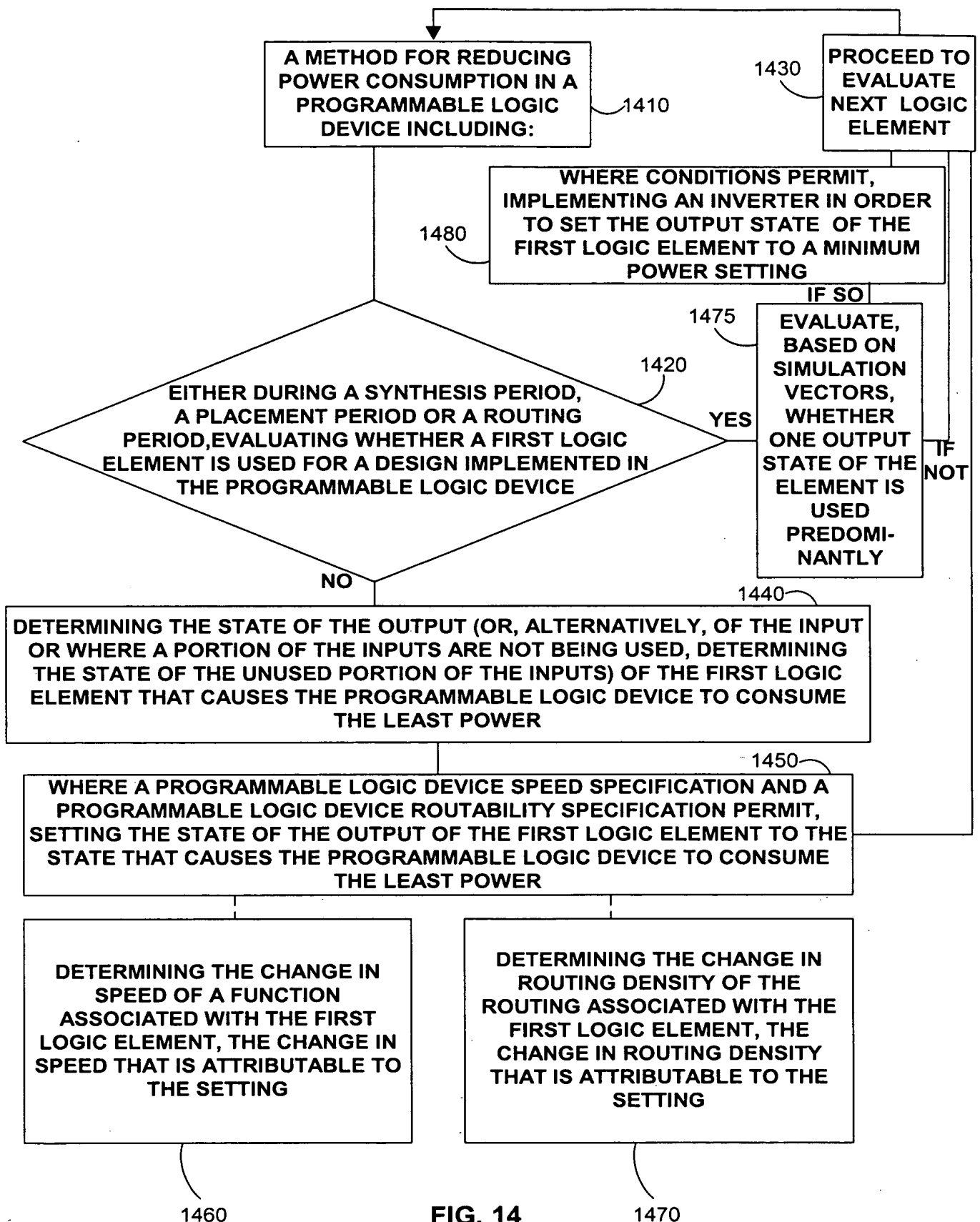


FIG. 14

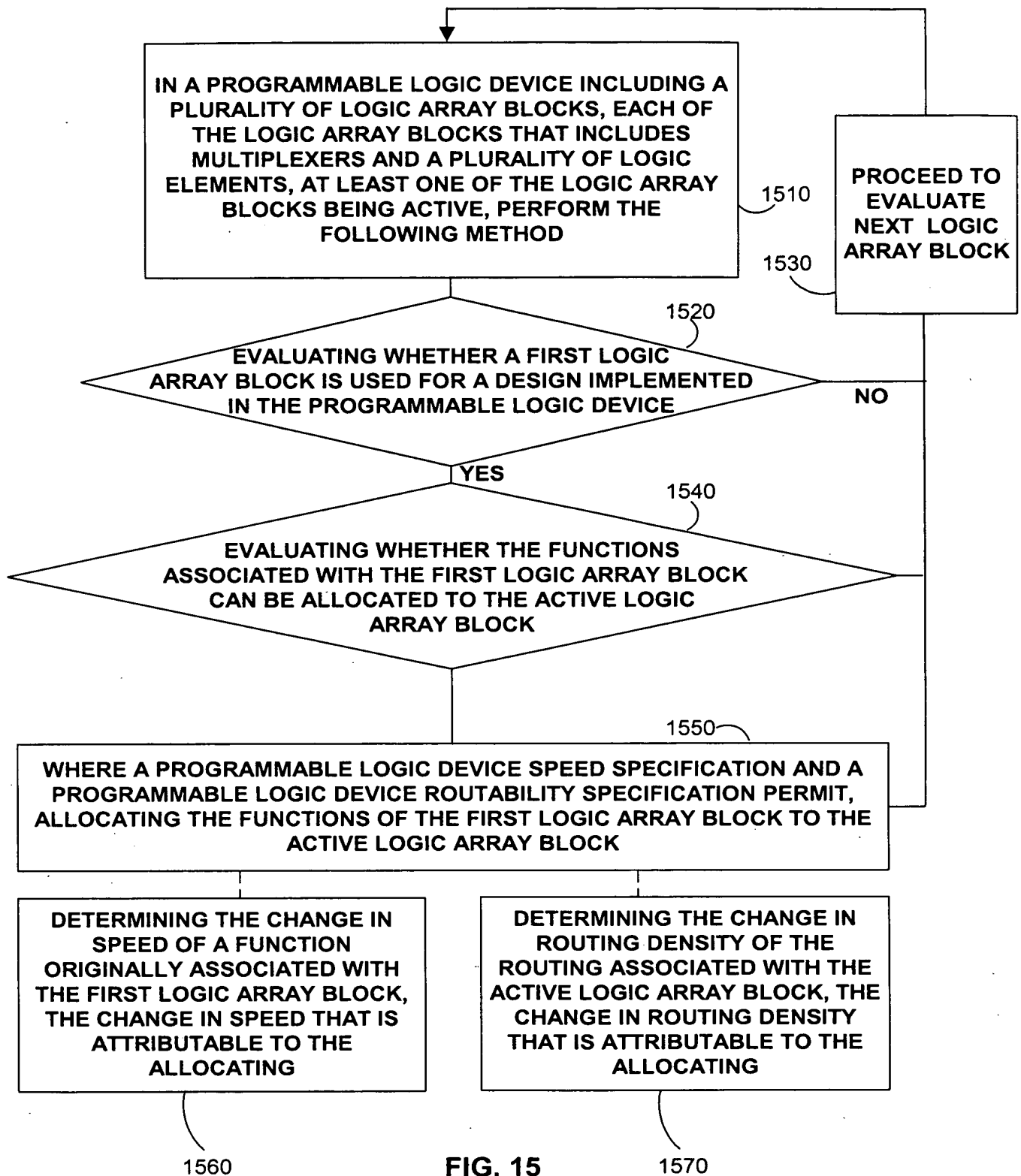
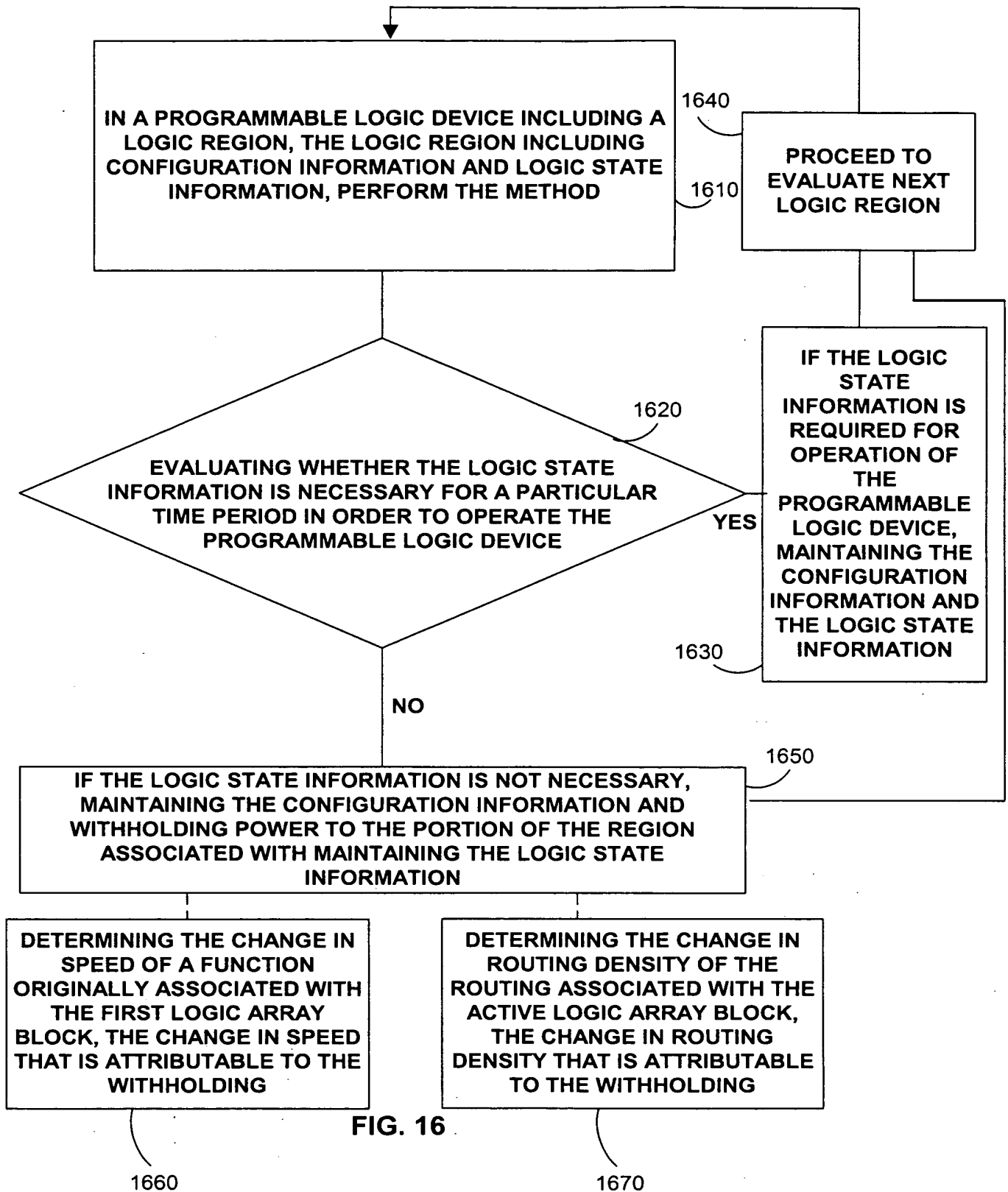
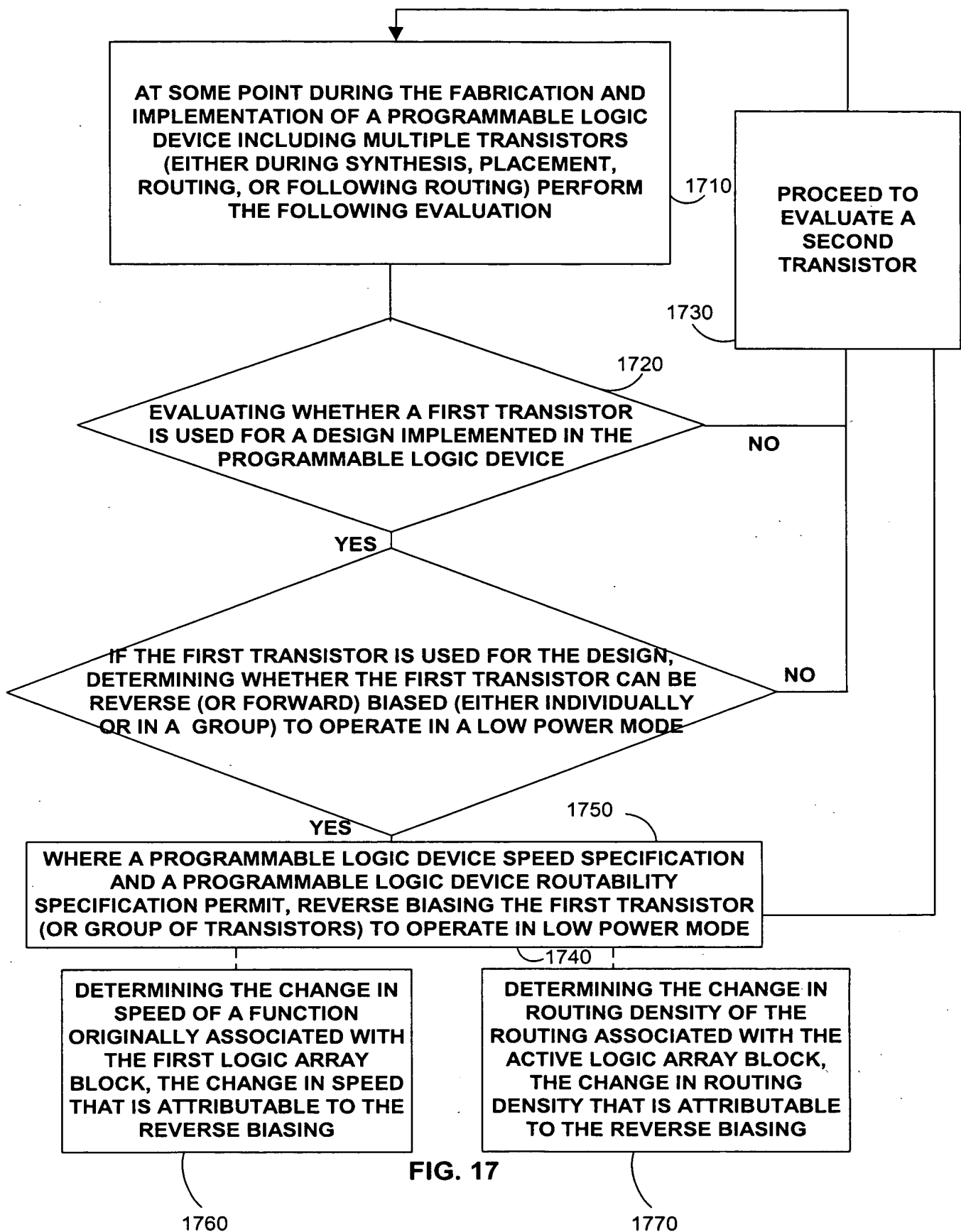
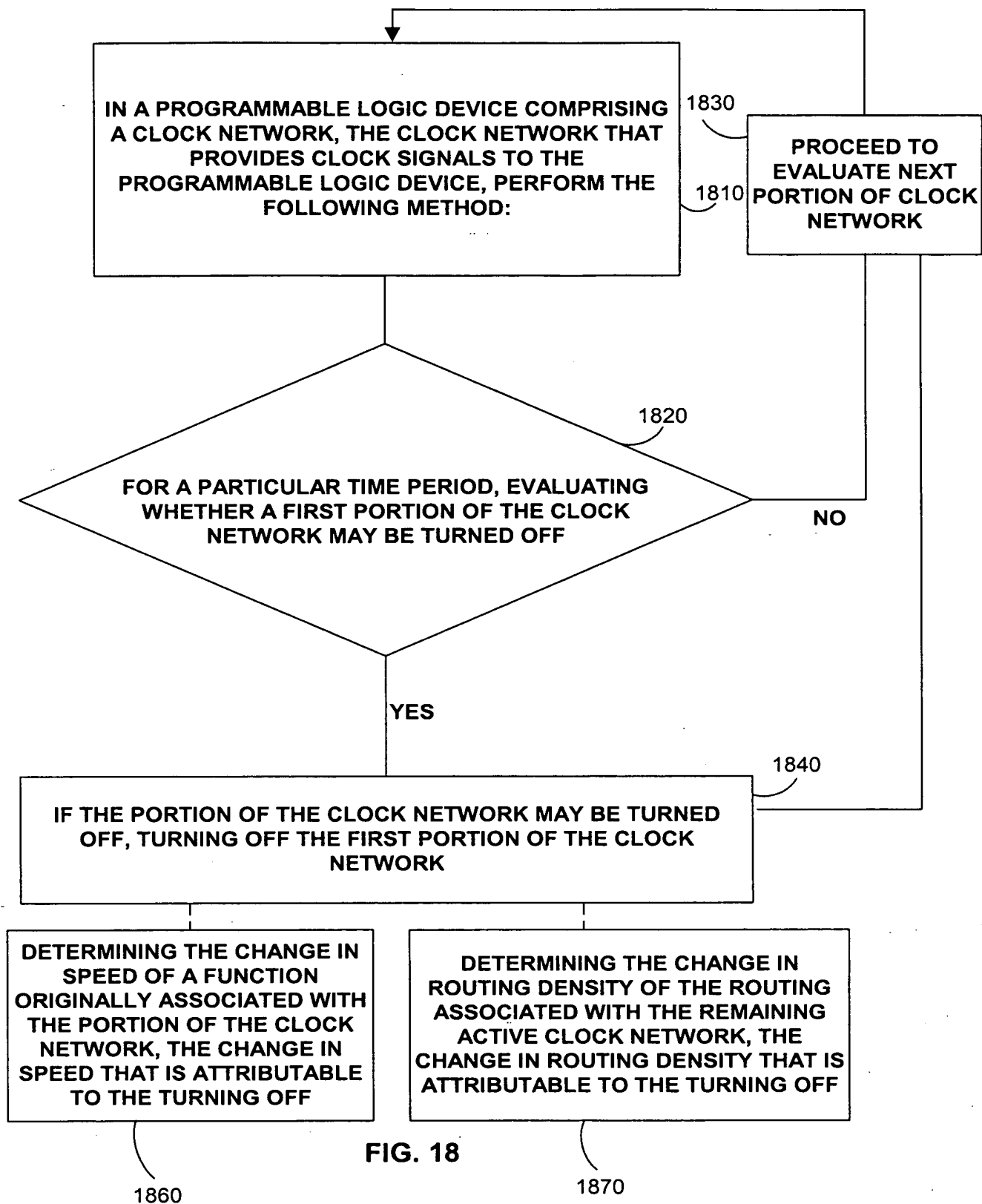


FIG. 15







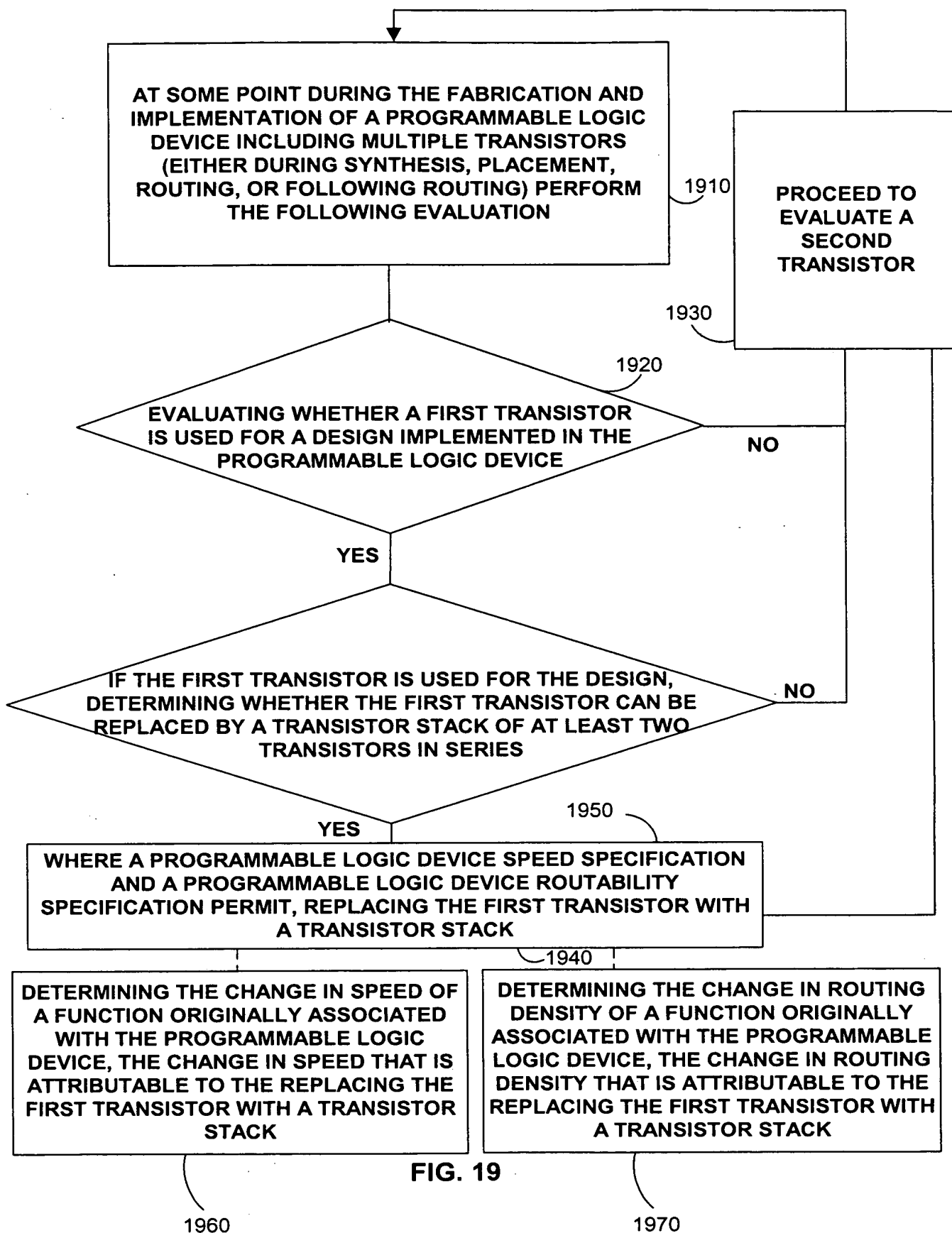


FIG. 19

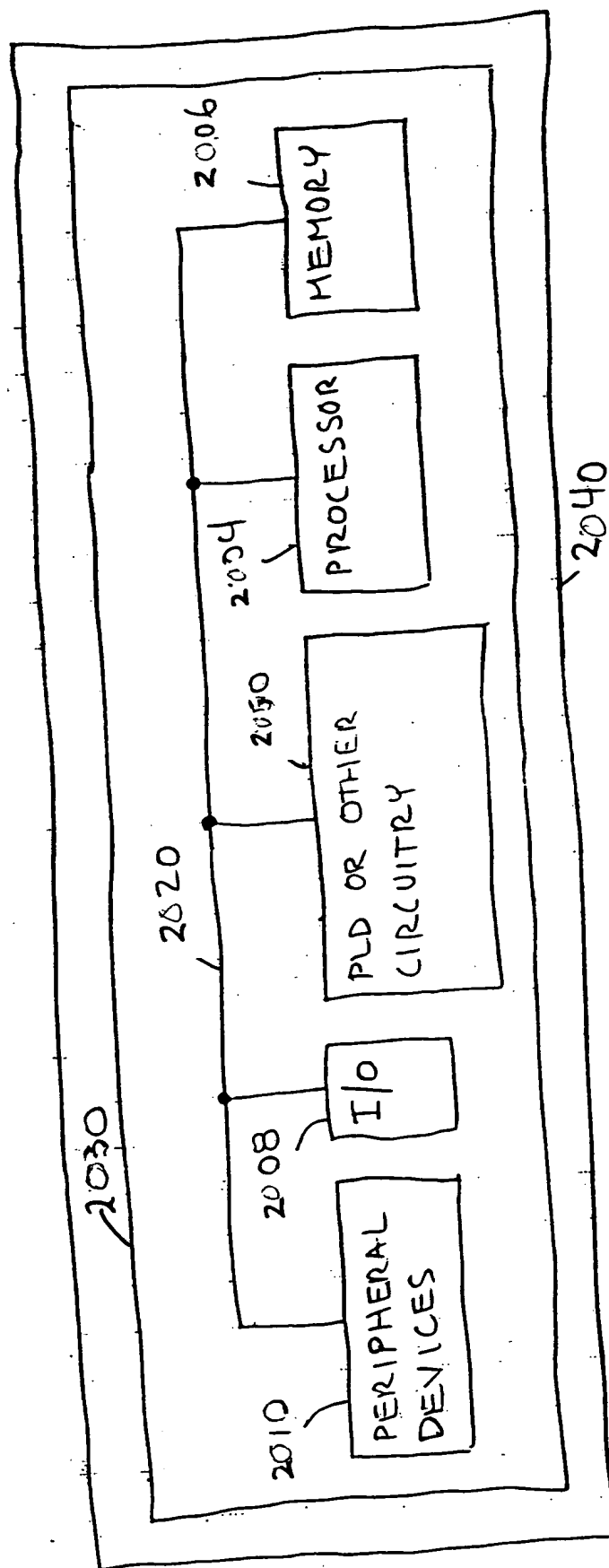


FIG. 20

2002

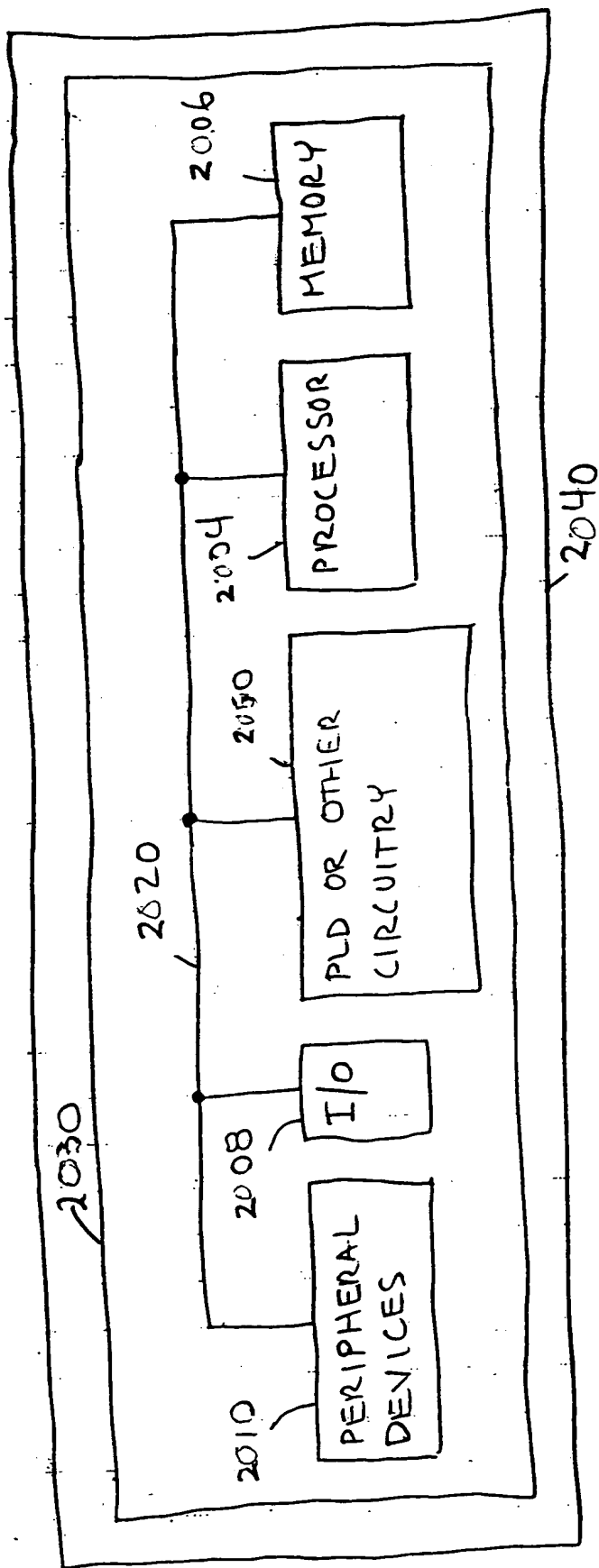


FIG. 20